	Application No.	Applicant(s)
	10/575,852	BARBOSA ET AL.
Notice of Allowability	Examiner	Art Unit
	Bao Q. Vu	2838
The MAILING DATE of this communication apperature All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in this apport or other appropriate communication GHTS. This application is subject to	plication. If not included n will be mailed in due course. THIS
1. This communication is responsive to		
2. The allowed claim(s) is/are <u>1-21</u> .		
3. ★ Acknowledgment is made of a claim for foreign priority unapprint a) ★ All b) Some* c) None of the:	der 35 U.S.C. § 119(a)-(d) or (f).	
 Certified copies of the priority documents have 	been received.	
2. Certified copies of the priority documents have	been received in Application No	·
3. Copies of the certified copies of the priority do	cuments have been received in this	national stage application from the
International Bureau (PCT Rule 17.2(a)).		
* Certified copies not received:		
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONN THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		complying with the requirements
4. A SUBSTITUTE OATH OR DECLARATION must be subminformal PATENT APPLICATION (PTO-152) which give		
5. CORRECTED DRAWINGS (as "replacement sheets") mus	at be submitted.	
(a) ☐ including changes required by the Notice of Draftspers		-948) attached
1) hereto or 2) to Paper No./Mail Date		
(b) ☐ including changes required by the attached Examiner's Paper No./Mail Date		Office action of
Identifying Indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in t	.84(c)) should be written on the drawi he header according to 37 CFR 1.121(ngs in the front (not the back) of id).
6. DEPOSIT OF and/or INFORMATION about the depo attached Examiner's comment regarding REQUIREMENT		
Attachment(s) 1. ☑ Notice of References Cited (PTO-892)	5. ☐ Notice of Informal F	Patent Application
Notice of Preferences Cited (110-032) Notice of Draftperson's Patent Drawing Review (PTO-948)	6. Interview Summary	• •
	Paper No./Mail Da 7. ☐ Examiner's Amend	ite
3. Information Disclosure Statements (PTO/SB/08), Paper No./Mail Date 100 100 100 100 100 100 100 100 100 10	_	ent of Reasons for Allowance
 Examiner's Comment Regarding Requirement for Deposit of Biological Material 	_	CIT OF REASONS FOR Allowance
	9.	Jan 2
	(BAO Q. VU PRIMARY EXAMINER

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DETAILED ACTION

Allowable Subject Matter

- 1. Claims 1-21 are allowed.
- 2. The following is an examiner's statement of reasons for allowance: None of the prior art alone or in combination discloses an inverter circuit having a are provided for each phase (R, Y, B), with the n-th first switching group being formed by a first power semiconductor switch and a second power semiconductor switch, and with the first switching group to the (n-1)-th switching group each being formed by a first power semiconductor switch and a second power semiconductor switch and by a capacitor, which is connected to the first and second power semiconductor switches, with each of the n first switching groups being connected in series to the respectively adjacent first switching group, and with the first and the second power semiconductor switches in the first switching group being connected to one another, characterized in that wherein n > 1 and p second switching groups and p third switching groups are provided, which are each formed by a first power semiconductor switch and a second power semiconductor switch and by a capacitor which is connected to the first and second power semiconductor switches, where p > 1 and each of the p second switching groups is connected in series with the respectively adjacent second switching group, and each of the p third switching groups is connected in series with the respectively adjacent third switching group, and the first second switching group is connected to the first power semiconductor switch in the n-th first switching group, and the first third switching group

is connected to the second power semiconductor switch in the n-th first switching group, and in that the capacitor in the p-th second switching group is connected in series with the capacitor in the p-th third switching group."

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Bao Q. Vu whose telephone number is (571) 272-2088. The examiner can normally be reached on Monday-Thursdays, 8:00AM- 6:00PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Karl Easthom can be reached on (571) 272-2084. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Bao Q. Vu

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June 11, 2007